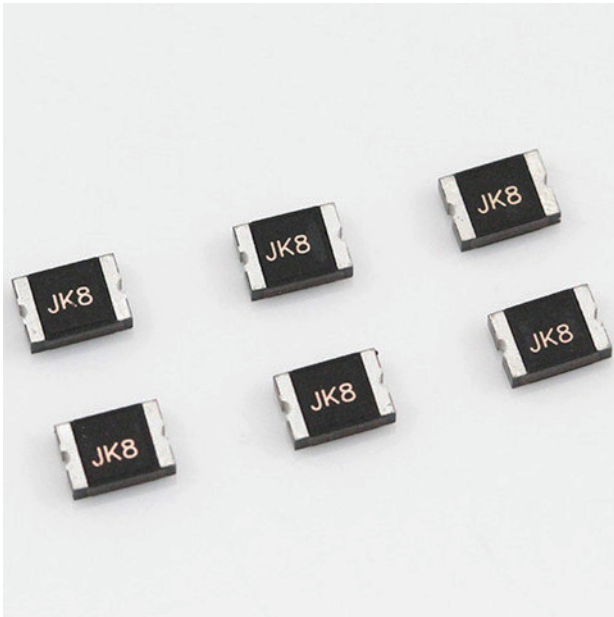


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PRODUCT DATASHEET

Surface Mount PTC Devices

ASMD1812SL Series Surface Mount PTC Devices



Description

The ASMD1812SL series provides surface mount resettable overcurrent protection with holding current from 1.9A to 9.0A.

This series offers complete portfolio in terms of holding current and working voltage, and is suitable for wide range of applications.

Features



- Ultra low resistance
- RoHS compliant and lead-free
- Halogen-free
- Fast response to fault current
- Symmetrical design

Agency Approvals

Agency	File Number
	Pending

Applications

- USB port protection - USB 2.0, 3.0&OTG
- Li-ion/Li-Polymer battery packs
- PDAs / digital cameras
- Computer peripherals
- PC motherboards-plug and play protection

Regulation	Standard
	2002/95/EC
	EN14582

Electrical Characteristics

Model	I _{hold} @25°C (A)	I _{trip} @25°C (A)	V _{max} (V)	I _{max} (A)	P _d Typ. (W)	Maximum Time To Trip		Resistance	
						Current (A)	Time (Sec)	R _{i min} (Ω)	R _{1max} (Ω)
ASMD1812SL190	1.9	3.8	6.0	50	1.5	8.0	5.0	0.003	0.025
ASMD1812SL190-12V	1.9	3.8	12.0	50	1.5	8.0	5.0	0.003	0.025
ASMD1812SL260	2.6	5.2	6.0	50	1.5	8.0	5.0	0.003	0.024
ASMD1812SL260-12V	2.6	5.2	12.0	50	1.5	8.0	5.0	0.003	0.024
ASMD1812SL300	3.0	6.0	6.0	50	1.5	15.0	2.0	0.003	0.022
ASMD1812SL300-12V	3.0	6.0	12.0	50	1.5	15.0	2.0	0.003	0.022
ASMD1812SL350	3.5	7.0	6.0	50	1.5	17.5	2.0	0.003	0.020
ASMD1812SL350-12V	3.5	7.0	12.0	50	1.5	17.5	2.0	0.003	0.020
ASMD1812SL400	4.0	8.0	6.0	50	1.8	20.0	2.0	0.003	0.018
ASMD1812SL400-12V	4.0	8.0	12.0	50	1.8	20.0	2.0	0.003	0.018
ASMD1812SL450	4.5	9.0	6.0	50	1.8	22.5	2.0	0.003	0.016
ASMD1812SL450-12V	4.5	9.0	12.0	50	1.8	22.5	2.0	0.003	0.016
ASMD1812SL500	5.0	10.0	6.0	50	1.8	25.0	2.0	0.003	0.014
ASMD1812SL500-12V	5.0	10.0	12.0	50	1.8	25.0	2.0	0.003	0.014
ASMD1812SL550	5.5	11.0	6.0	50	1.8	27.5	2.0	0.002	0.012
ASMD1812SL550-12V	5.5	11.0	12.0	50	1.8	27.5	2.0	0.002	0.012
ASMD1812SL600	6.0	12.0	6.0	50	1.8	30.0	2.0	0.002	0.010
ASMD1812SL600-12V	6.0	12.0	12.0	50	1.8	30.0	2.0	0.002	0.010
ASMD1812SL650	6.5	13.0	6.0	50	1.8	32.5	2.0	0.002	0.008
ASMD1812SL650-12V	6.5	13.0	12.0	50	1.8	32.5	2.0	0.002	0.008
ASMD1812SL700	7.0	14.0	6.0	50	2.0	35.0	2.0	0.001	0.007
ASMD1812SL700-12V	7.0	14.0	12.0	50	2.0	35.0	2.0	0.001	0.007
ASMD1812SL750	7.5	15.0	6.0	50	2.0	37.5	2.0	0.001	0.006
ASMD1812SL750-12V	7.5	15.0	12.0	50	2.0	37.5	2.0	0.001	0.006
ASMD1812SL800	8.0	16.0	6.0	50	2.0	40.0	2.0	0.0008	0.005
ASMD1812SL800-12V	8.0	16.0	12.0	50	2.0	40.0	2.0	0.0008	0.005
ASMD1812SL850	8.5	17.0	6.0	50	2.2	42.5	2.0	0.0008	0.004
ASMD1812SL850-12V	8.5	17.0	12.0	50	2.2	42.5	2.0	0.0008	0.004
ASMD1812SL900	9.0	18.0	6.0	50	2.2	45.0	2.0	0.0005	0.003
ASMD1812SL900-12V	9.0	18.0	12.0	50	2.2	45.0	2.0	0.0005	0.003

- I_{hold} = Hold Current. Maximum current device will not trip in 25°C still air.
 I_{trip} = Trip Current. Minimum current at which the device will always trip in 25°C still air.
 V_{max} = Maximum operating voltage device can withstand without damage at rated current (I_{max}).
 I_{max} = Maximum fault current device can withstand without damage at rated voltage (V_{max}).
 P_d = Power dissipation when device is in the tripped state in 25°C still air environment at rated voltage.
 $R_{i\ min/max}$ = Minimum/Maximum device resistance prior to tripping at 25°C.
 R_{1max} = Maximum device resistance is measured one hour post reflow.
CAUTION : Operation beyond the specified ratings may result in damage and possible arcing and flame.

Test Procedures and Requirements

Test	Conditions	Accept/Reject Criteria
Ri min	Resistance measurement at 25°C	$R_{imin} \leq R \leq R_{1max}$
R1 max	Resistance measurement one hour after post trip	$R_{imin} \leq R \leq R_{1max}$
I-hold	Hold rated current 1800s without trip, @ 25°C	No trip
I-trip	Device must trip within 900s under rated current, @25°C	Trip
Max. time to trip	At specified current, 25°C	$T \leq \text{max. Time to Trip(s)}$
Trip Cycle Life	V_{max} , I_{max} , 100 cycles	No arcing or burning
Trip Endurance	V_{max} , I_{max} 24 hours	No arcing or burning
Solderability	ANSI/J-STD-002	95% min. coverage

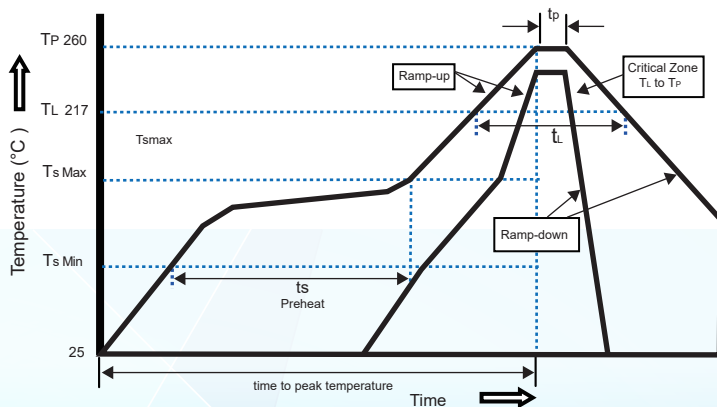
Environmental Specifications

Conditions	Resistance change
Operating Temperature	-40 °C to +85 °C
Max. Device Surface Temperature in Tripped State	125°C
Passive Aging	+85 °C, 1000 h ; ±10 % typical resistance change
Humidity Aging	+85 °C, 85 % R.H. 100 h; ±15 % typical resistance change
Thermal Shock	MIL-STD-202, Method 107; +85 °C to -40 °C, 20 times;-30 % typical resistance change
Solvent Resistance	MIL-STD-202, Method 215 ; No change
Vibration	MIL-STD-883, Method 2007, Condition A; No change
Moisture Sensivity Level	Level 2, J-STD-020
Storage Conditions	+30 °C Max. 60% RH Max. Packed in original packaging.

Thermal Derating Chart

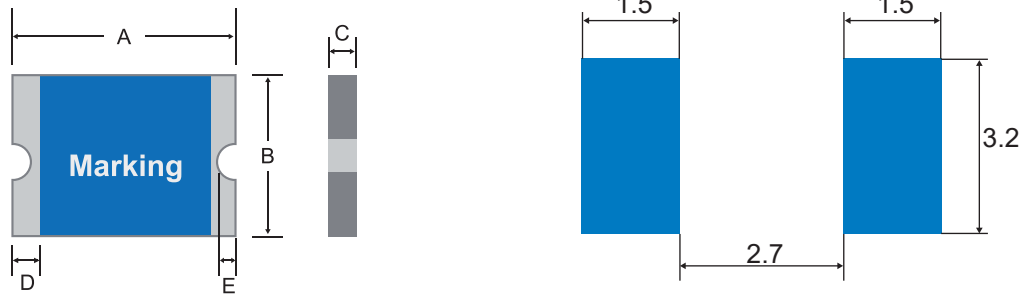
Model	Maximum ambient operating temperature (T_{mao}) vs. hold current (I_{hold})								
	- 40°C	- 20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
ASMD1812SL190	2.76	2.47	2.22	1.90	1.63	1.46	1.31	1.18	0.95
ASMD1812SL260	3.78	3.38	3.04	2.60	2.23	2.00	1.79	1.61	1.30
ASMD1812SL300	4.35	3.90	3.51	3.00	2.58	2.31	2.07	1.86	1.50
ASMD1812SL350	5.08	4.55	4.10	3.50	3.01	2.70	2.42	2.17	1.75
ASMD1812SL400	5.80	5.20	4.68	4.00	3.44	3.08	2.76	2.48	2.00
ASMD1812SL450	6.57	5.94	5.15	4.50	3.93	3.46	3.14	2.72	2.14
ASMD1812SL500	7.30	6.61	5.72	5.00	4.37	3.84	3.49	3.03	2.38
ASMD1812SL550	8.03	7.27	6.30	5.50	4.81	4.23	3.84	3.33	2.62
ASMD1812SL600	8.76	7.93	6.87	6.00	5.24	4.61	4.18	3.63	2.86
ASMD1812SL650	9.49	8.59	7.44	6.50	5.68	4.99	4.53	3.93	3.10
ASMD1812SL700	10.22	9.25	8.01	7.00	6.12	5.38	4.88	4.24	3.33
ASMD1812SL750	10.95	9.91	8.59	7.50	6.55	5.76	5.23	4.54	3.57
ASMD1812SL800	11.68	10.57	9.16	8.00	6.99	6.15	5.58	4.84	3.81
ASMD1812SL850	12.41	11.23	9.73	8.50	7.43	6.53	5.93	5.14	4.05
ASMD1812SL900	13.14	11.89	10.30	9.00	7.86	6.92	6.28	5.45	4.29

Soldering Parameters



Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate(T_{SMax} to T_p)	3°C/s Max.
-Temperature Min($T_{\text{s Min}}$)	150°C
Preheat -Temperature Max($T_{\text{s Max}}$)	200°C
-Time($t_{\text{s Min}}$ to $t_{\text{s Max}}$)	60~180 s
Time maintained -Temperature(T_L)	217°C
above: -Time(t_L)	60~150 s
Peak/Classification Temperature(T_p)	260 $_{+0/-5}$ °C
Time within 5°C of actual peak temperature(t_p)	20~40 s
Ramp down rate	6°C/s Max.
Time 25°C to peak temperature	8 min. Max.

- ◆ All temperature refer to topside of the package, measured on the package body surface
- ◆ If reflow temperature exceeds the recommended profile, devices may not meet the performance requirements
- ◆ Recommended reflow methods: IR, vapor phase oven, hot air oven, N2 environment for lead
- ◆ Recommended maximum paste thickness is 0.25mm (0.010inch)
- ◆ Devices can be cleaned using standard industry methods and solvents

Physical Dimensions(mm.)

Recommended Pad layout

Model	A		B		C		D		E
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.
ASMD1812SL190	4.37	4.73	3.07	3.41	0.30	0.70	0.30	1.20	0.15
ASMD1812SL190-12V	4.37	4.73	3.07	3.41	0.30	0.70	0.30	1.20	0.15
ASMD1812SL260	4.37	4.73	3.07	3.41	0.40	1.00	0.30	1.20	0.15
ASMD1812SL260-12V	4.37	4.73	3.07	3.41	0.40	1.00	0.30	1.20	0.15
ASMD1812SL300	4.37	4.73	3.07	3.41	0.40	1.00	0.30	1.20	0.15
ASMD1812SL300-12V	4.37	4.73	3.07	3.41	0.40	1.00	0.30	1.20	0.15
ASMD1812SL350	4.37	4.73	3.07	3.41	0.40	1.20	0.30	1.20	0.15
ASMD1812SL350-12V	4.37	4.73	3.07	3.41	0.40	1.20	0.30	1.20	0.15
ASMD1812SL400	4.37	4.73	3.07	3.41	0.40	1.20	0.30	1.20	0.15
ASMD1812SL400-12V	4.37	4.73	3.07	3.41	0.40	1.20	0.30	1.20	0.15
ASMD1812SL450	4.37	4.73	3.07	3.41	0.40	1.40	0.30	1.20	0.15
ASMD1812SL450-12V	4.37	4.73	3.07	3.41	0.40	1.40	0.30	1.20	0.15
ASMD1812SL500	4.37	4.73	3.07	3.41	0.40	1.40	0.30	1.20	0.15
ASMD1812SL500-12V	4.37	4.73	3.07	3.41	0.40	1.40	0.30	1.20	0.15
ASMD1812SL550	4.37	4.73	3.07	3.41	0.40	1.40	0.30	1.20	0.15
ASMD1812SL550-12V	4.37	4.73	3.07	3.41	0.40	1.40	0.30	1.20	0.15
ASMD1812SL600	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL600-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL650	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL650-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL700	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL700-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL750	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL750-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15

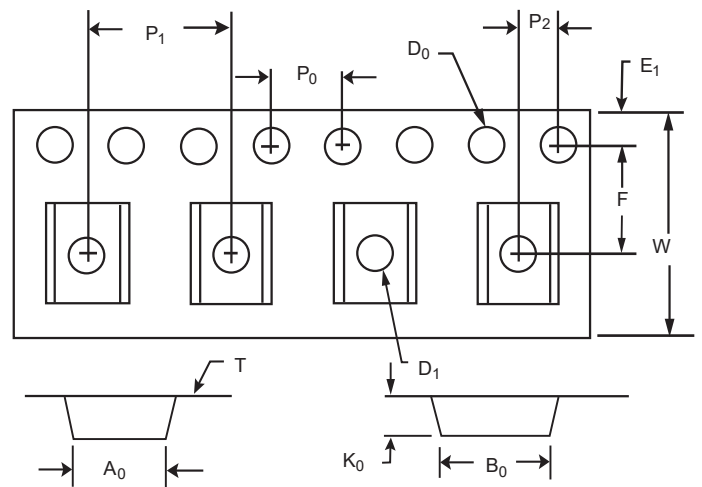
Model	A		B		C		D		E
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.
ASMD1812SL800	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL800-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL850	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL850-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL900	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15
ASMD1812SL900-12V	4.37	4.73	3.07	3.41	0.60	1.60	0.30	1.20	0.15

Terminal Material: Tin-Plated Nickle-Copper (Solder Material:Matte Tin (Sn)).

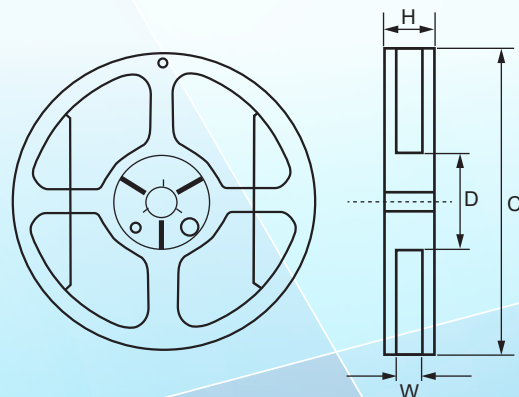
Lead Solderabilit: Meets EIA Specification RS186-9E, ANSI/J-STD-002 Category 3.

Tape And Reel Specifications (mm)

Governing Specifications	EIA 481-1
W	12.0 ± 0.10
F	5.50 ± 0.05
E1	1.75 ± 0.10
D0	1.55 ± 0.05
D1	1.5 Min
P0	4.00 ± 0.10
P1	8.00 ± 0.10
P2	2.00 ± 0.05
A0	3.58 ± 0.10
B0	4.93 ± 0.10
T	0.25 ± 0.05
K0	0.87/1.3± 0.10
Leader	390 mm
Trailer	160 mm



REEL DIMENSIONS: EIA-481-1(mm)	
C	φ 178 ± 1.0
D	φ 60.2 ± 0.5
W	13.2 ± 1.5
H	16.0 ± 0.5

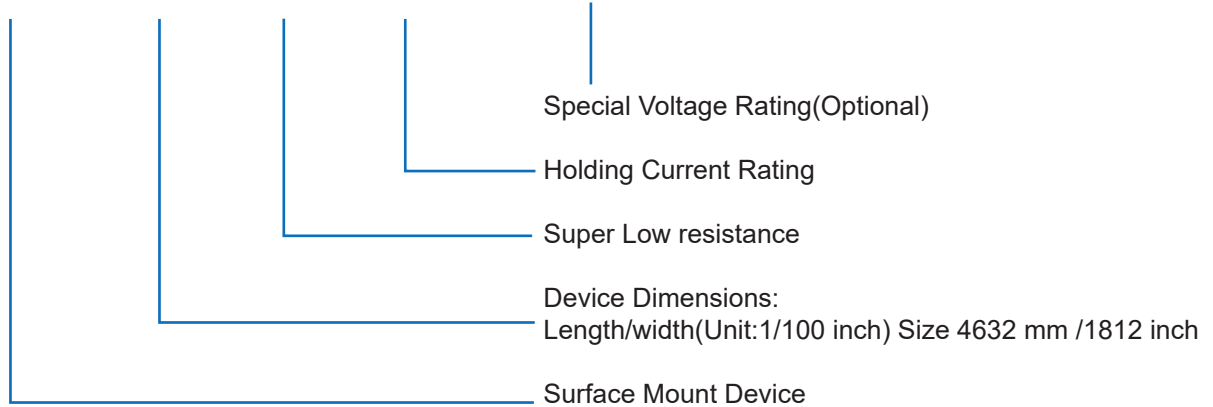


Packaging Quantity

Model	Quantity
ASMD1812SL190~550	2,000 pcs/reel
The Others	1,500 pcs/reel

Part Number System

ASMD 1812 SL -



Application Notice

1. Operation of these PPTC devices beyond the stated maximum ratings could result in damage to the devices and lead to electrical arcing and/or fire ;

PPTC 器件在超过规定的最大值额定值运行可能会导致器件损坏以及导致电弧和/或火灾。

2. These PPTC devices are intended to protect against the effects of temporary over-current or over-temperature conditions and shall not be taken for use as switch.

PPTC 的作用是防止临时的过流或过温造成的不良影响，不能当作开关使用。

3. Exposure to lubricants, silicon-based oils, solvents, gels, electrolytes, acids, and other related or similar materials may adversely affect the performance of PPTC devices.

PPTC 接触润滑剂、硅基油、溶剂、凝胶、电解质、酸和其他相关或类似材料可能会对 PPTC 器件的性能有不利影响。

4. Circuits with inductance may generate a voltage above the rated voltage of the PPTC device and should be thoroughly evaluated within the user's application during the PPTC selection and qualification process.

带有电感的电路可能产生高于 PPTC 额定电压的电压，因此客户在选型和认定过程中应进行彻底的评估。

5. Please do not smash, clamp, pull, dent or twist by tool during assembling process , as they may result in the PPTC damage.

在装配过程中，避免有砸、挤、拉、扭等方式外力作用于 PPTC 本体上，因为它们可能导致 PPTC 损坏。

6. Hand-soldering of PPTC devices on boards is generally not recommended. Users shall define and verify this process if needed.

不推荐使用手工焊接的方式焊接 PPTC。如果需要，用户需要定义和验证此过程。

7. Recommended storage conditions should be followed at all times , The MSL classification of PPTC is grade 2a.

必须始终遵守推荐的保存条件要求，PPTC 的 MSL 等级为 2 a级。